

	Type	L #	Hits	Search Text	DBs
1	BRS	L1	381	((tes (tacking))same ((gate\$3 logic)switch\$5))	USPA T
2	BRS	L2	42372 5	enabl\$5 disabl\$5	USPA T
3	BRS	L3	150	1 and 2	USPA T
4	BRS	L4	10613	((pattern sequence)same tracking)	USPA T
5	BRS	L5	17	3 and 4	USPA T
6	BRS	L6	217	((tes (tacking))same ((gate\$3 logic)switch\$5))	US-P GPUB ; EPO; JPO; DERW ENT; IBM_ TDB
7	BRS	L7	16778 77	enabl\$5 disabl\$5	US-P GPUB ; EPO; JPO; DERW ENT; IBM_ TDB
8	BRS	L8	90	6 and 7	US-P GPUB ; EPO; JPO; DERW ENT; IBM_ TDB
9	BRS	L9	19830 74	((pattern sequence)edge)	US-P GPUB ; EPO; JPO; DERW ENT; IBM_ TDB

	Time Stamp	Comments	Error Definition	Errors
1	2004/04/28 13:44			0
2	2004/04/28 13:39			0
3	2004/04/28 13:39			0
4	2004/04/28 13:48			0
5	2004/04/28 13:40			0
6	2004/04/28 13:44			0
7	2004/04/28 13:44			0
8	2004/04/28 13:45			0
9	2004/04/28 13:45			0

	Type	L #	Hits	Search Text	DBs
10	BRS	L10	76	8 and 9	US-P GPUB ; EPO; JPO; DERW ENT; IBM_ TDB
11	BRS	L11	16787 55	((pattern sequence) edge)	USPA T
12	BRS	L12	133	3 and 11	USPA T

	Time Stamp	Comments	Error Definition	Errors
10	2004/04/28 13:45			0
11	2004/04/28 13:48			0
12	2004/04/28 13:49			0